





Correction

<b>Substitute for Form 1449/PTO</b> <b>INFORMATION DISCLOSURE</b> <b>STATEMENT BY APPLICANT</b> (use as many sheets as necessary)		<b>Complete if Known</b>			
		Application Number	10/611,379		
		Filing Date	06/30/2003		
		First Named Inventor:	Buxton, et al.		
		Art Unit	2193		
		Examiner Name	David H. Malzahn		
Sheet	20	of	25	Attorney Docket Number	42P15761
<b>NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T <sup>2</sup>
/DHM/		MO, Hyeon-Cheol, et al., <i>A High-Speed Pattern Decoder in MPEG-4 Padding Block Hardware Accelerator</i> , 0-7803-6685-9/01, IEEE, 2001, pp. II-197 - II-200.			
/DHM/		MOSCHETTI, F., et al., <i>About Macroblock Subsampling for Motion Estimation on IA-64</i> , Proc. of 2001 IEEE Int'l. Conf. on Multimedia and Expo (ICME 2001), Tokyo, Japan, August 2001, 4 pp.			
/DHM/		MOSCHETTI, F., et al., <i>A Fast Block Matching for SIMD Processors Using Subsampling</i> , IEEE #0-7803-5482-6/99, pp. IV-321 - IV-324. 5/2006			
/DHM/		NAM, Kwon Moon, Joon-Seek Kim, Rae-Hong Park, Young Serk Shim, <i>A Fast Hierarchical Motion Vector Estimation Algorithm Using Mean Pyramid</i> , IEEE Transactions on Circuits and Systems on Video Technology, Vol. 5, No. 4, Aug. 1995, pp. 344-351.			
/DHM/		NETRAVALI, A., B. Haskell, <i>Digital Pictures Representation and Compression</i> , New York, Plenum, 1988, pp. cover-xv, 334-340, 537-542, and 354-355.			
/DHM/		PIRSCH, Peter, Nicolas Demassieux, Winfried Gehrke, <i>VLSI Architectures for Video Compression - A Survey</i> , Proceedings of the IEEE, Vol. 83, No. 2, Feb. 1995, pp. 220-246.			
/DHM/		PO, Lai-Man, Wing-Chung Ma, <i>A Novel Four-Step Search Algorithm for Fast Blockmatching</i> , IEEE Transactions on Circuits and Systems on Video Technology, Vol. 6, No. 3, Jun. 1996, pp. 313-317.			
/DHM/		PURI, A., H.M. Hang, D.L. Schilling, <i>An Efficient Blockmatching Algorithm for Motion Compensated Coding</i> , Proc. IEEE ICASSP, 1987, pp. 2.4.1-25.4.4.			
/DHM/		RAGSDALE, Gary L., et al, <i>Relationships of Popular Transmission Characteristics to Perceived Quality for Digital Video Over ATM</i> , National Communications System, Technical Information Bulletin 99-2, January 1999, 64 pp.			
/DHM/		RAMKISHOR, K., et al., <i>Real Time Implementation of MPEG-4 Video Decoder on ARM7TDMI</i> , Proc. of 2001 Int'l. Symposium on Intelligent Multimedia, Video and Speech Processing, May 2-4, 2001, pp. 522-526.			
/DHM/		SHI, Y.Q., X. Xia, <i>A Threshold Multiresolution Block Matching Algorithm</i> , IEEE Transactions on Circuits and Systems on Video Technology, Vol. 7, No. 2, April 1997, pp. 437-440.			

Examiner Signature	/David Malzahn/	Date Considered	06/26/2007
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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